



CAMPBELL & PARTNERS
EUROPEAN RECRUITMENT CONSULTANTS

Ref: GK/1358

FPGA Gigabit Design Engineer

Client:

Leading manufacturer of image and video acquisition components for the machine vision, healthcare, ITS and video surveillance markets and with products combining high-performance software and hardware developments based on the latest microelectronics technology. For German HQ, we are now looking for a young professional to further strengthen the organization.

Location: South West of Munich

Responsibilities:

- hardware development for vision applications including cameras, embedded vision systems, video transport solutions and customer-specific solutions
- assistance with the development of IP Cores for vision applications
- specification, documentation and maintenance of IP Cores
- verification of IP Cores via functional testing and simulation
- provision of support and on-site training to customers
- development of applications in FPGA (VHDL) and in CPU (ANSI C)
- in charge of new projects involving gigabit data transmission and MIPI CSI/2
- provision of support to international sales teams
- you report to the local Head of Development

Requirements:

- a high-achieving graduate in electronics
- at least 3 years' experience in FPGA development in image or video processing
- proficiency in English and German
- FPGA: VHDL for Xilinx or Intel/Altera
- Tools: ModelSim, Vivado, Quartus
- High-speed busses: PCIe, MIPI CSI/2, SLVDS, AXI, CoaXPress, Gigabit Ethernet, USB3...
- Programming: C, makefiles, tcl-tk

Please e-mail your application to: first@campbell.de